IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:)	
Benjamin S. Ting) Examiner:	Not yet assigned
Application No.: Not yet assigned) Art Unit:	Not yet assigned
Filed: Herewith)	
For: ARCHITECTURE AND INTERCONNECT SCHEME FOR PROGRAMMABLE LOGIC CIRCUITS))))	
a Continuation of)	
Application No.: 10/428,724)	
Filed: May 1, 2003) _) _)	

Commissioner for Patents PO Box 1450 Alexandria, Virginia 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to the examination of the above-referenced application on the merits it is respectfully requested that the following amendments be entered.

